



Certificate of compliance

Applicant: Renac Power Technology Co., Ltd
Building 6, No.2, West Jinzhi Road, High-Tech District,
Suzhou City, Jiangsu Province
China

Product: Photovoltaic (PV) and battery inverter

Model: ESC3000-DS
ESC3680-DS

Use in accordance with regulations:

Automatic disconnection device with single-phase mains surveillance in accordance with Engineering Recommendation G98/1 for photovoltaic systems with a single-phase parallel coupling via an inverter in the public mains supply. The automatic disconnection device is an integral part of the aforementioned inverter. This serves as a replacement for the disconnection device with isolating function that can access the distribution network provider at any time.

Applied rules and standards:

Engineering Recommendation G98/1-4:2019

Requirements for the connection of Fully Type Tested Micro-generators (up to and including 16 A per phase) in parallel with public Low Voltage Distribution Networks

DIN V VDE V 0126-1-1:2006-02 (4.1 Functional safety)

Automatic disconnection device between a generator and the public low-voltage grid

At the time of issue of this certificate the safety concept of an aforementioned representative product corresponds to the valid safety specifications for the specified use in accordance with regulations.

Report number: ABRE-ESH-P20030501

Certification program: NSOP-0032-DEU-ZE-V01

Certificate number: U20-0354

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Certification body

Thomas Lammel

Certification body Bureau Veritas Consumer Products Services Germany GmbH accredited according to DIN EN ISO/IEC 17065
A partial representation of the certificate requires the written approval of Bureau Veritas Consumer Products Services Germany GmbH

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98/1

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Type Approval and declaration of compliance with the requirements of Engineering Recommendation G98/1.

PGM Technology	Photovoltaic (PV) and battery inverter		
Manufacturer	Renac Power Technology Co., Ltd		
Address	Building 6, No.2, West Jinzhi Road, High-Tech District, Suzhou City, Jiangsu Province, China		
Tel	0512-66677278	Fax	---
Email	info@renacpower.com	Website	www.renacpower.com

Rated values	ESC3000-DS	ESC3680-DS
MPP DC voltage range [V]	100-550	
Input DC voltage [V]	580	
Input DC current [A]	12/12	
Output AC voltage [V]	230Vac, 50/60Hz	
Output AC current [A]	13	16
Output power [VA]	3000	3680

Firmware version	01
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Measurement period:	2020-03-10 - 2020-04-10
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Description of the structure of the power generation unit:

The power generation unit is equipped with a PV and line-side EMC filter. The power generation unit has galvanic isolation between DC input and AC output. Output switch-off is performed with single-fault tolerance based on two series-connected relays in line and neutral. This enables a safe disconnection of the power generation unit from the network in case of error.

The above stated Generating Units are tested according the requirements in the Engineering Recommendation G98/1. Any modification that affects the stated tests must be named by the manufacturer/supplier of the product to ensure that the product meets all requirements of the Engineering Recommendation G98/1.

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Operating Range.

Test 1	Voltage = 85% of nominal (195,5 V) Frequency = 47.5 Hz Power Factor = 1 Period of test 90 minutes
Connection:	Always connected
Limit:	Always connected
Test 2	Voltage = 110% of nominal (253 V) Frequency = 51.5 Hz Power Factor = 1 Period of test 90 minutes
Connection:	Always connected
Limit:	Always connected
Test 3	Voltage = 110% of nominal (253 V) Frequency = 52.0 Hz Power Factor = 1 Period of test 15 minutes
Connection:	Always connected
Limit:	Always connected

Protection. Voltage tests.

Phase 1

Function	Setting		Trip test		No trip test	
	Voltage [V]	Time delay [s]	Voltage [V]	Time delay [s]	Voltage / time	Confirm no trip
U/V	184,0	2,5	183,3	2,64	188V / 5s	No trip
					180V / 2,45s	No trip
O/V stage 1	262,2	1,0	264,2	1,15	258,2V 5,0s	No trip
O/V stage 2	273,7	0,5	274,9	0,65	269,7V 0,95s	No trip
					277,7V 0,45s	No trip

Note. For Voltage tests the Voltage required to trip is the setting $\pm 3,45V$. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting $\pm 4V$ and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

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Protection. Frequency tests.						
Function	Setting		Trip test		No trip test	
	Frequency [Hz]	Time delay [s]	Frequency [Hz]	Time delay [s]	Frequency / time	Confirm no trip
U/F stage 1	47,5	20	47,41	20,2	47,7Hz / 30s	No trip
U/F stage 2	47,0	0,5	47,01	0,644	47,2Hz / 19,5s	No trip
					46,8Hz / 0,45s	No trip
O/F stage 2	52,0	0,5	52,07	0,550	51,8Hz / 120s	No trip
					52,2Hz / 0,45s	No trip

Note. For Frequency Trip tests the Frequency required to trip is the setting $\pm 0,1\text{Hz}$. In order to measure the time delay a larger deviation than the minimum required to operate the protection can be used. The "No-trip tests" need to be carried out at the setting $\pm 0,2\text{Hz}$ and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Protection. Loss of Mains.						
Inverters tested according to BS EN 62116.						
Balancing load on islanded network	33% of -5% Q Test 22	66% of -5% Q Test 12	100% of -5% P Test 5	33% of +5% Q Test 31	66% of +5% Q Test 21	100% of +5% P Test 10
Trip time. Ph1 fuse removed [s]	0,350	0,380	0,260	0,260	0,195	0,450

Note. Trip time limit is 0,5s. For technologies which have a substantial shut down time this can be added to the 0,5s in establishing that the trip occurred in less than 0,5s maximum. Shut down time could therefore be up to 1,0s for these technologies.

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Protection. Re-connection timer.

Test should prove that the reconnection sequence starts in no less than 20 seconds for restoration of voltage and frequency to within the stage 1 settings of table 2.

Over Voltage				
Time delay setting		Measured delay		
20s		75,0s		
Under Voltage				
Time delay setting		Measured delay		
20s		77,0s		
Over Frequency				
Time delay setting		Measured delay		
20s		77,0s		
Under Frequency				
Time delay setting		Measured delay		
20s		77,0s		
	Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of table 1.			
	At 266,2V	At 180,0V	At 47,4Hz	At 52,1Hz
Confirmation that the Generating Unit does not re-connect.	No reconnection	No reconnection	No reconnection	No reconnection

Protection. Frequency change, Stability test.

	Start Frequency [Hz]	Change	Test Duration	Confirm no trip
Positive Vector Shift	49,0	+50 degrees		No trip
Negative Vector Shift	50,0	-50 degrees		No trip
Positive Frequency drift	49,0 to 51,0	+0,95Hz/sec	2,1s	No trip
Negative Frequency drift	51,0 to 49,0	-0,95Hz/sec	2,1s	No trip

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Limited Frequency Sensitive Mode – Over Frequency

1-min mean value [Hz]:	a) 50,00	b) 50,45	c) 50,70	d) 51,15	e) 50,70	f) 50,45	g) 50,00
1. Measurement a) to g): Active power output > 80% P_n							
Frequency [Hz]:	50,00	50,45	50,70	51,15	50,70	50,45	50,00
P_{expected} [kW]:	N/A	3606,4	2944,0	2281,6	2944,0	3606,4	N/A
P_{measured} [kW]:	3661,7	3553,3	3222,2	2550,5	3210,6	3554,2	3670,7
2. Measurement a) to g): Active power output 40% and 60% after freezing > 80% P_n							
Frequency [Hz]:	50,00	50,45	50,70	51,15	50,70	50,45	50,00
P_{expected} [kW]:	N/A	1803,2	1472,0	1140,8	1472,0	1803,4	N/A
P_{measured} [kW]:	1876,7	1802,3	1631,4	1267,2	1615,7	1785,3	1853,8

Output Power with falling Frequency			
5-min mean value (each)	a) 50 ± 0,01 Hz	b) - 0,4 to - 0,5 Hz	c) - 2,4 to - 2,5 Hz
Frequency [Hz]:	50,00	49,60	47,60
Active power [W]:	3654,7	3670,5	3674,8
ΔP/P_{max} [%]:			0,05

Note.

Electronic inverter therefore no power reduction.

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Power Quality. Harmonics.

ESC3680-DS

SSEG rating per phase (rpp)						
	At 45-55% of rated output 1,82kW		100% of rated output 3,66kW			
Harmonic	Measured Value (MV) in [A]	Measured Value (MV) in [%]	Measured Value (MV) in [A]	Measured Value (MV) in [%]	Limit in BS EN61000-3-2 in Amps	Higher limit for odd harmonics 21 and above
2nd	0,0345	0,436	0,0849	0,537	1,080	
3rd	0,2647	3,349	0,4317	2,733	2,300	
4th	0,0105	0,133	0,0182	0,115	0,430	
5th	0,0752	0,951	0,0982	0,622	1,140	
6th	0,0035	0,044	0,0033	0,021	0,300	
7th	0,0359	0,454	0,0589	0,373	0,770	
8th	0,0029	0,037	0,0044	0,028	0,230	
9th	0,0350	0,443	0,0334	0,212	0,400	
10th	0,0031	0,040	0,0038	0,024	0,184	
11th	0,0107	0,136	0,0336	0,213	0,330	
12th	0,0023	0,029	0,0033	0,021	0,153	
13th	0,0209	0,265	0,0157	0,099	0,210	
14th	0,0027	0,035	0,0029	0,018	0,131	
15th	0,0056	0,071	0,0233	0,148	0,150	
16th	0,0015	0,019	0,0020	0,013	0,115	
17th	0,0150	0,189	0,0060	0,038	0,132	
18th	0,0020	0,026	0,0024	0,015	0,102	
19th	0,0043	0,055	0,0188	0,119	0,118	
20th	0,0015	0,019	0,0022	0,014	0,092	
21th	0,0116	0,146	0,0025	0,016	0,107	0,160
22th	0,0012	0,015	0,0015	0,010	0,084	
23th	0,0038	0,048	0,0144	0,091	0,098	0,147
24th	0,0015	0,018	0,0017	0,011	0,077	
25th	0,0098	0,124	0,0039	0,025	0,090	0,135
26th	0,0012	0,016	0,0017	0,011	0,071	
27th	0,0033	0,042	0,0110	0,070	0,083	0,124
28th	0,0008	0,010	0,0015	0,010	0,066	
29th	0,0086	0,109	0,0046	0,029	0,078	0,117
30th	0,0018	0,022	0,0020	0,013	0,061	
31th	0,0024	0,031	0,0077	0,049	0,073	0,109
32th	0,0005	0,007	0,0013	0,008	0,058	
33th	0,0076	0,096	0,0055	0,035	0,068	0,102
34th	0,0008	0,010	0,0010	0,007	0,054	
35th	0,0028	0,035	0,0056	0,035	0,064	0,096
36th	0,0010	0,013	0,0014	0,009	0,051	
37th	0,0066	0,083	0,0062	0,039	0,061	0,091
38th	0,0007	0,009	0,0006	0,004	0,048	
39th	0,0034	0,043	0,0035	0,022	0,058	0,087
40th	0,0006	0,008	0,0012	0,007	0,046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

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Power Quality. Power factor.

Output power	216,2V	230V	253V	Measured at three voltage levels and at full output. Voltage to be maintained within $\pm 1,5\%$ of the stated level during the test.
20%	0,9693	0,9628	0,9515	
50%	0,9938	0,9929	0,9916	
75%	0,9968	0,9966	0,9961	
100%	0,9978	0,9975	0,9975	
Limit	>0,95	>0,95	>0,95	

Power Quality. Voltage fluctuation and Flicker.

	Starting			Stopping			Running	
	dmax	dc	d(t)	dmax	dc	d(t)	Pst	Plt 2 hours
Measured values at test impedance	0,58	0,47	0	0,60	0,47	0	0,16	0,15
Measured values at standard impedance	0,58	0,47	0	0,60	0,47	0	0,16	0,15
Limits set under BS EN 61000-3-3	4%	3,3%	3,3% 500ms	4%	3,3%	3,3% 500ms	1,0	0,65
Test impedance	R	0,4	Ω	XI		0,25	Ω	
	Z	0,472	Ω					
Standard impedance	R	0,4	Ω	XI		0,25	Ω	
	Z	0,472	Ω					

Power Quality. DC injection.

Test level power [%]	20	50	75	100
Recorded value [mA]	18,8	2,2	22,7	24,9
Recorded value [%]	0,12	0,01	0,14	0,16
Limit [%]	0,25	0,25	0,25	0,25

Note. DC-injection is tested at each phase of the inverter and a limit of 0,25% per phase was used as pass criteria.

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Fault level Contribution.

For a directly coupled SSEG			For a Inverter SSEG		
Parameter	Symbol	Value	Time after fault	Volts [V]	Amps [A]
Peak Short Circuit current	I_p	N/A	20ms	140,0V	17,92A
Initial Value of aperiodic current	A	N/A	100ms	N/A	N/A
Initial symmetrical short-circuit current*	I_k	N/A	250ms	N/A	N/A
Decaying (aperiodic) component of short circuit current*	i_{DC}	N/A	500ms	N/A	N/A
Reactance/Resistance Ratio of source*	X/R	N/A	Time to Trip [ms]	19,4	

For rotating machines and linear piston machines the test should produce a 0s – 2s plot of the short circuit current as seen at the Generating Unit terminals.

* Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot.

Self Monitoring – Solid state switching.	N/A
It has been verified that in the event of the solid state switching device failing to disconnect the Generating Unit, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0,5 seconds.	
Note. Unit do not provide solid state switching relays. In case the semiconductor bridge is switched off, then the voltage on the output drops to 0. In this case the relays on the output will also open (Functional safety of the internal automatic disconnection device according to VDE 0126-1-1).	

Logic Interface (input port) Required by paragraph 11.1.3	P
Confirm that an input port is provided and can be used to shut down the module.	Yes